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## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
Stylesheet Version v18.0

<b>Title of Invention</b>	<b>High-Voltage Transistor With Multi-Layered Conduction Region</b>																												
<p>Application Number: 09/961229 Confirmation Number: 4375 First Named Applicant: Vladimir Rumennik Attorney Docket Number: 03692.P007XD4 Art Unit: 2811 Examiner: Shouxiang Hu Search string: ( 4614959 or 5294824 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>Init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>4614959</td><td>1986-09-01</td><td>Nakagawa</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>5294824</td><td>1994-03-01</td><td>Okada</td><td></td><td></td><td></td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td></td></tr></tbody></table>		Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	4614959	1986-09-01	Nakagawa					2	5294824	1994-03-01	Okada				Examiner Name	Date		
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List of Patents and Publications Statement (Use several sheets if necessary)				<b>Applicants:</b>			
				Rumennik, V., et al.			
				<b>Filing Date:</b> September 20, 2001			
<b>REFERENCE DESIGNATION</b>				<b>U.S. PATENT DOCUMENTS</b>			
<b>Exam. Initial</b>		<b>Date</b>	<b>Document Number</b>	<b>Name</b>	<b>Class</b>	<b>Sub-Class</b>	<b>Filing Date</b>
		06/04/91	5,021,858	Hall			
		03/18/03	6,534,829 B2	Sogo, et al.			
<b>FOREIGN PATENT DOCUMENTS</b>							
<b>N .</b>		<b>Document No.</b>	<b>Date</b>	<b>Country</b>	<b>Name</b>	<b>Class</b>	<b>Sub-Class</b>
		JP 5-21791	01-1993	Japan	Ueda		
		EP 0 295 391 A1	04-1988	EPO	Eklund		
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<b>Examiner</b>				<b>Date Considered</b>			
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